Amendments to the Claims

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims

1. (Currently Amended) An image processing apparatus comprising:

a reception ~~means for receiving plural~~ unit adapted to receive at least three encoded image data;

a ~~main frame generation means for~~ decoding unit adapted to decode one of said ~~plural~~ encoded image data to generate a main frame;

a sub frame generation ~~means for extracting~~ united adapted to generate sub frames using a low frequency component extracted from each one of ~~said plural~~ the other encoded image data ~~to generate a sub frame~~; and

~~output means for outputting~~ an image signal generation unit adapted to generate an image signal including said main frame and said sub ~~frame~~ frames.

2. (Currently Amended) An apparatus according to claim 1, wherein said reception ~~means~~ unit receives said ~~plural~~ at least three encoded image data through a serial bus.

3. (Currently Amended) An apparatus according to claim 2, wherein said serial bus is based on the ~~IEEE1394-1995~~ IEEE 1394-1995 standard.

4. (Currently Amended) An apparatus according to claim 1, wherein said reception ~~means~~ unit is a digital interface based on the ~~IEEE1394-1995~~ IEEE 1394-1995 standard.

2

5. (Currently Amended) An apparatus according to claim 1, further comprising:

a switch ~~means for switching~~ unit adapted to switch the encoded image data ~~displayed on~~ corresponding to said main frame and the encoded image data corresponding to one of said sub frames, in response to an operation of a predetermined operation key.

6. (Currently Amended) An apparatus according to claim 1, further comprising:

a recording ~~means for recording~~ unit adapted to record the encoded image data ~~displayed on~~ corresponding to said main frame on a storage medium, in response to an operation of a predetermined operation key.

7. (Currently Amended) An apparatus according to claim 1, wherein said ~~plural~~ at least three encoded image data are based on the SD format of the DV standard.

8. (Currently Amended) An image processing method comprising steps of:

receiving ~~plural~~ at least three encoded image data;

decoding one of said ~~plural~~ encoded image data to generate a main frame;

~~extracting~~ generating sub frames using a low frequency component extracted from each one of ~~said plural~~ the other encoded image data ~~to generate a sub-frame~~; and

~~outputting~~ generating an image signal including said main frame and said sub ~~frame~~ frames.

9. (Currently Amended) A method according to claim 8, wherein said reception step receives said ~~plural~~ at least three encoded image data through a serial bus.

10. (Currently Amended) A method according to claim 9, wherein said serial bus is based on the ~~IEEE1394-1995~~ IEEE 1394-1995 standard.

3

11. (Currently Amended) A method according to claim 8, wherein said plural image data are received through a digital interface based on the ~~IEEE1394-1995~~ <u>IEEE 1394-1995</u> standard.

12. (Currently Amended) A method according to claim 8, further comprising <u>a step of</u>:

~~a step for~~ switching the <u>encoded</u> image data <u>~~displayed on~~</u> <u>corresponding to</u> said main frame <u>and the encoded image data corresponding to one of said sub frames</u>, in response to an operation of a predetermined operation key.

13. (Currently Amended) A method according to claim 8, further comprising <u>a step of</u>:

~~a step for~~ recording the <u>encoded</u> image data <u>~~displayed on~~</u> <u>corresponding to</u> said main frame <u>on a storage medium</u>, in response to an operation of a predetermined operation key.

14. (Currently Amended) A method according to claim 8, wherein said ~~plural~~ <u>at least three</u> <u>encoded</u> image data are based on the SD format of the DV standard.

4